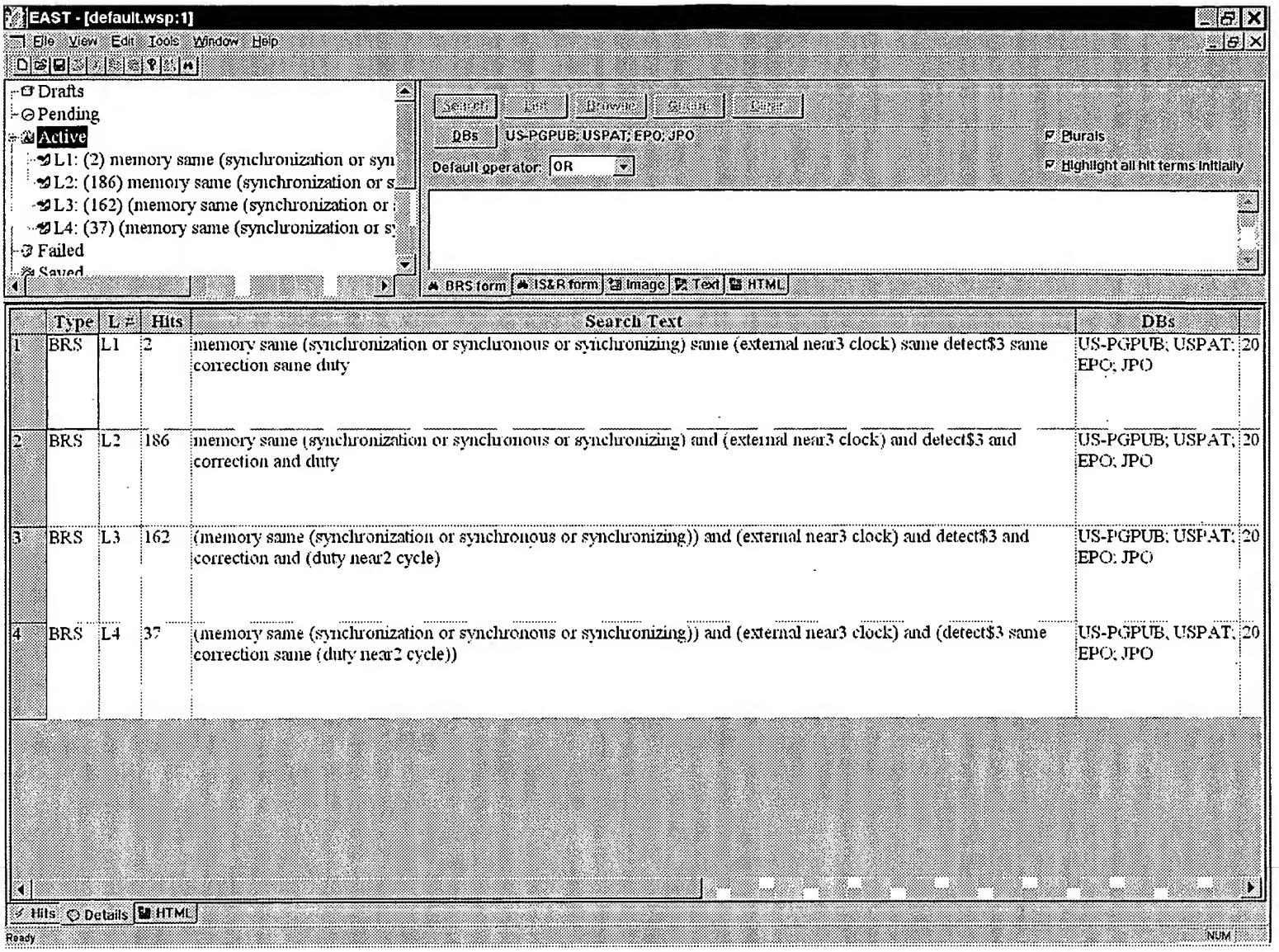
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1 BRS L1 2 memory same (synchronization or synchronous or synchronizing) same (external near3 clock) same detect$3 same correction same duty US-PGPUB; USPAT; EPO; JPO 20
2 BRS L2 186 memory same (synchronization or synchronous or synchronizing) and (external near3 clock) and detect$3 and correction and duty US-PGPUB; USPAT; EPO; JPO 20
3 BRS L3 162 (memory same (synchronization or synchronous or synchronizing)) and (external near3 clock) and detect$3 and correction and (duty near2 cycle) US-PGPUB; USPAT; EPO; JPO 20
4 BRS L4 37 (memory same (synchronization or synchronous or synchronizing)) and (external near3 clock) and (detect$3 same correction same (duty near2 cycle)) US-PGPUB; USPAT; EPO; JPO 20
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